

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT : Avinash SODANI, et al.  
SERIAL NO. : 10/749,272  
FILED : 30 December 2003  
FOR : METHOD AND APPARATUS FOR RESCHEDULING OPERATIONS IN  
A PROCESSOR  
Patent No. : **7,502,912 B2** Issued 10 March 2009

COMMISSIONER FOR PATENTS  
P. O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR CERTIFICATE OF CORRECTION**  
**PURSUANT TO 37 C.F.R. § 1.322**

SIR:

It is respectfully requested that the enclosed certificate of correction be issued for the above Patent under authority of 35 USC §354.

The change represents correction of an error which occurred during printing of the patent and was not the fault of the applicants. Therefore, no fee is required.

Respectfully submitted,

Dated: 13 April 2010

/Shawn W. O'Dowd/  
Shawn W. O'Dowd  
(Reg. No. 34,687)

KENYON & KENYON LLP  
1500 K Street, N.W., Suite 700  
Washington, DC 20005

Tel: (202) 220-4200  
Fax: (202) 220-4201

UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

**PATENT NO.** : 7,502,912 B2 Page 1 of 1  
**DATED** : 10 March 2009  
**FILED** : 30 December 2003  
**INVENTOR(S)** : Avinash SODANI, et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

<u>Column</u>	<u>Line</u>	
6	50-51	After "the delay" insert --period--.

**MAILING ADDRESS OF SENDER:**

Shawn W. O'Dowd  
KENYON & KENYON LLP  
1500 K Street, N.W., Suite 700  
Washington, DC 20005

**Patent No.: 7,502,912 B2**